

PATENT
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of: Wang)

Serial No.: 10/602,229)

Filed: June 24, 2003)

For: Novel Dual Bit Split Gate Flash)
Memory)

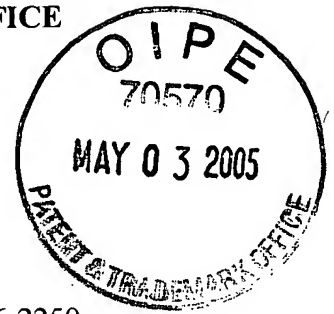
Group Art Unit: 2818

Examiner: Huynh, Andy

Confirmation No. 8541

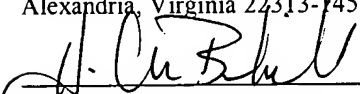
TKHR Docket No.: 252016-2250

Top-Team Ref. 0503-8775US



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RESPONSE TO NOTICE UNDER 37 CFR 1.251

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Sir:

Enclosed is a complete and accurate copy of the Information Disclosure Statement and Power of Attorney dated September 12, 2003, from our file.

Respectfully submitted,



Daniel R. McClure, Reg. No. 38,962

THOMAS, KAYDEN,
HORSTEMEYER & RISLEY, L.L.P.
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In re Application of: WANG, SHIH-WEI
Application No.: 10602229
Filing Date: 06/24/03
Title: NOVEL DUAL BIT SPLIT GATE FLASH MEMORY

Direct to: U. S. Patent and Trademark Office
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☐ The copy of the paper(s) listed in the notice under 37 CFR 1.251 is/are a complete and accurate copy of applicant's record of such paper(s).

☐ The papers produced by applicant are applicant's complete record of all of the correspondence between the Office and the applicant for the above-identified application (except for U.S. patent documents), and applicant is not aware of any correspondence between the Office and the applicant for the above-identified application that is not among applicant's records.

☐ Applicant does not possess any record of the correspondence between the Office and the applicant for the above identified application.

April 29, 2005
Date


Signature

Daniel R. McClure
Typed or printed name

A Copy of this notice should be returned with the reply.

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Serial No.: 10/602,229

Filed: June 24, 2003

Patent No.:

Issued:

Applicant: Wang

Title: Novel Dual Bit Split Gate Flash Memory

Docket No.: 252016-2250

C/M Date: 04/29/05

EM No.:

Due Date:

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Person Mailing: HCB

Responsible Atty: DRM

